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| **Project Name: Ariane 5** | |
| **Test Case for Simulations** | |
| **Test Case ID:** Flight 501 | **Test Designed by:** <Lions> |
| **Test Priority (Low/Medium/High):** High | **Test Designed date:** <1996> |
| **Module Name:** Strap-Down Inertial Platform Module | **Test Executed by:** <Lions> |
| **Test Title:** Flight Data Simulation | **Test Execution date:** <1996> |
| **Description:** Use simulation data to verify exception handling |  |
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| **Pre-conditions:** All modules function up to this module. | |
| **Dependencies:** How will exceptions in this module affect other modules. | |

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| **Step** | **Test Steps** | **Test Data** | **Expected Result** | **Actual Result** | **Status (Pass/Fail)** | **Notes** |
| 1 | Simulate flight data which will produce exceptions | Simulate large horizontal bias values | Module should send exception to on-board computer and it should be handled correctly. | Exception handled | Pass |  |
| 2 | Simulate Operand Error | Simulate floating point values that need to be converted to 16-bit signed values | Module should send exception to on-board computer and it should be handled correctly | Exception not handled | Fail |  |
| 3 |  |  |  |  |  |  |
| 4 |  |  |  |  |  |  |
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**Post-conditions:**

Large flight data values that result in exceptions need to be handled with a diagnostic bit pattern. The diagnostic bit pattern needs to be recognized by the on-board computer and properly handled.